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FIS9-2001-0156
IB3-0009

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Edelstein et al.)
Serial No.:	10/005,861) Group Art Unit: 1762
Filed:	11/08/2001)
For:	LOW K DIELECTRIC FILM DEPOSITION PROCESS) Examiner: Fuller, Eric B.

VIA FACSIMILE: (703) 872-9326

Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

DECLARATION UNDER 37 CFR §1.131

We, Daniel C. Edelstein, William J. Cote, Timothy J. Dalton, Christopher V. Jahnes, and Gill Yong Lee, declare and state that:

1. We are the inventors of the invention claimed in the above-identified patent application.
2. We conceived and reduced to practice in the United States the invention disclosed and claimed in the above-identified patent application sometime prior to June 18, 2001.
3. As evidence in support of this prior conception and reduction to practice,

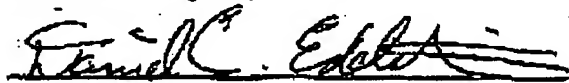
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submitted herewith is the following evidence of activity done in the United States.


(a) Exhibit A is a copy of an invention disclosure submitted internally to the Legal Department of IBM in the United States sometime prior to June 18, 2001. Exhibit A clearly provides support for the conception and reduction to practice of the claimed low k dielectric film deposition processes.

4. The undersigned declares that all statements made herein of his own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of this application or any patent issuing thereon.


Date: 12/9/04


Daniel C. Edelstein

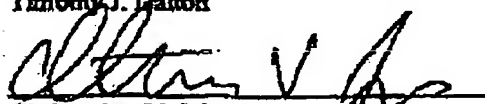
Date: 12-6-04


William J. Cote

Date: 12/17/04


Timothy J. Dalton

Date: 12/14/04
12/13/04


Christopher V. James

Date: 12/27/04


Gail Yong Lee